



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/655,595 Confirmation No.: 9321
Applicant : William Beausoleil, et al.
Filing Date : September 6, 2000
Title : High Speed Software Driven Emulator Comprised of a Plurality
of Emulation Processors With Improved Board-to-Board
Interconnection Cable Length Identification System
Group Art Unit : 2123
Examiner : Thomas H. Stevens
Docket No. : 700693-1208
Customer No. : 34313

Mail Stop RCE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 CFR §§ 1.97 and 1.98, the items identified in this Information Disclosure Statement ("IDS") are brought to the attention of the Office. The items are listed on the attached form PTO-1449 and copies are enclosed for the convenience of the Examiner.

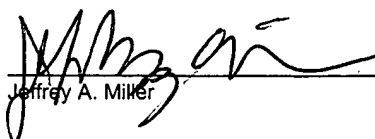
The items identified in this IDS may or may not be "material" pursuant to 37 CFR § 1.56. The submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as "prior art" under 35 USC § 102 with respect to this invention unless specifically designated by Applicant as such.

CERTIFICATE OF MAILING (37 CFR §1.8)

I hereby certify, pursuant to 37 CFR §1.8, that I have reasonable basis to expect that that this paper or fee (along with any referred to as being attached or enclosed) would be mailed or transmitted on or before the date indicated with the United States Postal Service with sufficient postage as first class mail on the date shown below in an envelope addressed to Mail Stop Patent Application, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Dated: October 28, 2004

DOCSSVI:234052.2
706316-1227 R1H



Jeffrey A. Miller

Applicant : William Beausoleil, et al.
Appl. No. : 09/655,595
Examiner : Thomas H. Stevens
Docket No. : 706316-1208

INFORMATION DISCLOSURE STATEMENT FILING PROVISION:

☒ This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d); or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.

☒ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.

☐ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.

☐ This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.

☐ The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.

☐ This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.

Applicant : William Beausoleil, et al.
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STATEMENT UNDER 37 CFR § 1.97(e):

☐ Each item contained in this IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.

☐ No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.

PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:

☐ A check in the amount of ___ is enclosed for the above fee(s).

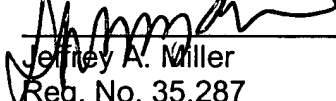
☐ Please charge _____ Deposit Account No. **15-0665** for the above fee(s).

The Commissioner is authorized to charge any fees required by the filing of these papers, and to credit any overpayment to Orrick, Herrington & Sutcliffe's Deposit Account No. **15-0665**.

Respectfully submitted,

ORRICK, HERRINGTON & SUTCLIFFE LLP

Dated: October 28, 2004

By: 
Jeffrey A. Miller
Reg. No. 35,287

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FORM PTO-1449

ATTY. DOCKET NO.

706316-1208

SERIAL NO.

09/655,595

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT

APPLICANT:

William F. Beausoleil, et al.

FILING DATE:

September 6, 2000

GROUP:

2123

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	AA	5,551,013	08/27/96	W.F. Beausoleil, et al	395	500	06/03/1994
	AB	6,618,698	09/09/2003	W.F. Beausoleil, et al	703	23	08/12/1999

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	BA							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	CA	U.S. Patent Application Serial No. 09/656,147, Filed Sept. 6, 2000; Entitled: High Speed Software Driven Emulator Comprised Of A Plurality Of Emulation Processors With An Improved Maintenance Bus That Streams Data At High Speed; W.F. Beausoleil, et al.
	CB	U.S. Patent Application Serial No. 09/656,541, Filed Sept. 6, 2000; Entitled: High Speed Software Driven Emulator Comprised Of A Plurality Of Emulation Processors With A Method To High Speed Bulk Read/Writes Operation Synchronous Dram While Refreshing The Memory; W.F. Beausoleil, et al.
	CA	U.S. Patent Application Serial No. 09/596,596, Filed Sept. 6, 2000; Entitled: High Speed Software Driven Emulator Comprised Of A Plurality Of Emulation Processors With A Method To Allow Memory Read/Writes Without Interrupting The Emulation; W.F. Beausoleil, et al.
	CD	U.S. Patent Application Serial No. 09/656,146, Filed Sept. 6, 2000; Entitled: High Speed Software Driven Emulator Comprised Of A Plurality Of Emulation Processors With Improved Multiplexed Data Memory; W.F. Beausoleil, et al